

## コア - 機能 #3269

### 共有メモリ型データポート接続機能

2015/07/23 09:01 - n-ando

ステータス:	終了	開始日:	2015/07/23
優先度:	通常	期日:	
担当者:	n-miyamoto	進捗率:	100%
カテゴリ:		予定工数:	0.00時間
対象バージョン:	RELEASE_1_2_0		
<b>説明</b>			
同一ノード内のコンポーネント間のデータポート接続において、データのPutを共有メモリ経由で行うことで、TCP/IPスタックを経由することなく高速にデータ転送する機能を実装する。			

#### 関係しているリビジョン

リビジョン 3057 - 2017/11/02 10:39 - kawauchi

[incompat,->RELENG\_1\_2] Convert from Shift-JIS to UTF-8-BOM. refs #3269

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リビジョン 3059 - 2017/11/02 18:38 - kawauchi

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[incompat,->RELENG\_1\_2] refs #3269

リビジョン 3062 - 2017/11/06 22:08 - miyamoto

[compat,bugfix] refs #3269

リビジョン 3062 - 2017/11/06 22:08 - miyamoto

[compat,bugfix] refs #3269

リビジョン 3073 - 2017/11/16 10:40 - kawauchi

[incompat,bugfix,->RELENG\_1\_2] '-lrt' option has been added. refs #3269

リビジョン 3073 - 2017/11/16 10:40 - kawauchi

[incompat,bugfix,->RELENG\_1\_2] '-lrt' option has been added. refs #3269

リビジョン 3074 - 2017/11/16 17:10 - kawauchi

[incompat,bugfix,->RELENG\_1\_2] '-lrt' option has been added. refs #3269

リビジョン 3074 - 2017/11/16 17:10 - kawauchi

[incompat,bugfix,->RELENG\_1\_2] '-lrt' option has been added. refs #3269

リビジョン 3109 - 2017/12/10 15:37 - n-ando

merged changes from trunk/OpenRTM-aist r3057-3062, 3065, 3070-3074, 3076-3083, 3086-3087 during 2017-11:

[incompat,->RELENG\_1\_2] Convert from Shift-JIS to UTF-8-BOM. refs #3269

[incompat,->RELENG\_1\_2] Shared memory data port implementation. #3269

[incompat,->RELENG\_1\_2] Shared memory data port implementation. refs #3269

[incompat,->RELENG\_1\_2] Python 3 support.

[incompat,bugfix,->RELENG\_1\_2] revision 3060.

[compat,bugfix,->RELENG\_1\_2] Shared memory data port implementation. refs #3269

[compat,->RELENG\_1\_1] Public key registration has been added. refs #4221

[compat,->RELENG\_1\_2] bug fix.

[compat,->RELENG\_1\_1] fixed bug. refs #4221

[compat,->RELENG\_1\_2] bug fix.

[incompat,bugfix,->RELENG\_1\_2] '-lrt' option has been added. refs #3269

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[incompat,bugfix,->RELENG\_1\_2] Modified Logger.idl.  
[incompat,bugfix,->RELENG\_1\_2] Modified Logger.idl.  
[incompat,->RELENG\_1\_2] Extension modules under src/ext have been added to dist packages. refs #4368  
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[compat,->RELENG\_1\_2] bug fix.  
[compat,->RELENG\_1\_2] bug fix.  
[build,->RELENG\_1\_2] Update fluent-bit's include path. refs #4373  
[incompat,->RELENG\_1\_2] LogicalTimeTriggeredEC has been added to build under Windows. refs #4368  
[incompat,->RELENG\_1\_2] LogicalTimeTriggeredEC has been added to build under Windows. refs #4368

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## 履歴

#1 - 2016/02/15 13:17 - n-ando

- 題名を共有メモリ型データポート接続 から 共有メモリ型データポート接続機能 に変更

同一ノード内のコンポーネント間のデータポート接続において、マーシャリング後のデータのPutを共有メモリ経由で行うことで、TCP/IPスタックを経由することなく高速にデータ転送する機能を実装すること。

#2 - 2017/10/31 17:49 - n-miyamoto

- ステータスを新規 から 担当 に変更

- 担当者を n-ando から n-miyamoto に変更

- 進捗率を 0 から 80 に変更

動作はPython版( [#3410](#) )と同じ。

coilに以下のファイルを追加

- SharedMemory.h  
- SharedMemory.cpp

Windows、Linuxは対応済み。VxWorksは中身のない関数を書いたファイルを置いている。

rtmに以下のファイルを追加。

- SharedMemoryPort.h  
- SharedMemoryPort.cpp  
- InPortSHMConsumer.h  
- InPortSHMConsumer.cpp  
- InPortSHMProvider.h  
- InPortSHMProvider.cpp  
- OutPortSHMConsumer.h  
- OutPortSHMConsumer.cpp  
- OutPortSHMProvider.h  
- OutPortSHMProvider.cpp

Push型、Pull型両方に対応。

CMakeでビルドできることを確認。

Automakeは未確認。一応Makefile.amにファイル名は追加したが、gccで-lrtのオプションが無いと未定義の関数でエラーになるためおそらく失敗する。

#3 - 2017/11/16 13:23 - n-miyamoto

- 進捗率を 80 から 100 に変更

#4 - 2017/11/16 13:23 - n-miyamoto

- ステータスを 担当 から 解決 に変更

#5 - 2018/09/13 09:32 - n-miyamoto

- ステータスを 解決 から 終了 に変更